|   | L# | Hits | S arch Text                                     | DBs                            |
|---|----|------|-------------------------------------------------|--------------------------------|
| 1 | L1 | 3122 | (361/760,782,783,784,7<br>85,786,790,803).ccls. | USP<br>AT;<br>US-P<br>GPU<br>B |
| 2 | L2 | 2507 | (365/51,52,63,185.09).c<br>cls.                 | USP<br>AT;<br>US-P<br>GPU<br>B |
| 3 | L3 | 605  | (710/301,302).ccls.                             | USP<br>AT;<br>US-P<br>GPU<br>B |
| 4 | L4 | 951  | 326/30.ccls.                                    | USP<br>AT;<br>US-P<br>GPU<br>B |
| 5 | L5 | 694  | (257/664,728).ccls.                             | USP<br>AT;<br>US-P<br>GPU<br>B |
| 6 | L6 | 7782 | 12345                                           | USP<br>AT;<br>US-P<br>GPU<br>B |
| 7 | L7 | 618  | 6 and @pd>=20021201                             | USP<br>AT;<br>US-P<br>GPU<br>B |

|    | L # | Hits | S arch T xt                       | DBs                            |
|----|-----|------|-----------------------------------|--------------------------------|
| 8  | L8  | 143  | 7 and terminat\$3                 | USP<br>AT;<br>US-P<br>GPU<br>B |
| 9  | L9  | 104  | 8 and (impedance\$1<br>match\$3)  | USP<br>AT;<br>US-P<br>GPU<br>B |
| 10 | L10 | 39   | 8 not 9                           | USP<br>AT;<br>US-P<br>GPU<br>B |
| 11 | L11 | 475  | 7 not 8                           | USP<br>AT;<br>US-P<br>GPU<br>B |
| 12 | L12 | 164  | 11 and (impedance\$1<br>match\$3) | USP<br>AT;<br>US-P<br>GPU<br>B |
| 13 | L13 | 26   | 7 and terminations                | USP<br>AT;<br>US-P<br>GPU<br>B |
| 14 | L14 | 161  | 12 not 13                         | USP<br>AT;<br>US-P<br>GPU<br>B |

|    | L#  | Hits       | S arch Text                       | DBs                            |
|----|-----|------------|-----------------------------------|--------------------------------|
| 15 | L15 | 309        | 11 not (13 14)                    | USP<br>AT;<br>US-P<br>GPU<br>B |
| 16 | L16 | 617        | 361/777.ccls.                     | USP<br>AT;<br>US-P<br>GPU<br>B |
| 17 | L17 | 1491       | 174/261.ccls.                     | USP<br>AT;<br>US-P<br>GPU<br>B |
| 18 | L18 | 141        | 16 and terminat\$4                | USP<br>AT;<br>US-P<br>GPU<br>B |
| 19 | L19 | 61         | 18 and (impedance\$1<br>match\$3) | USP<br>AT;<br>US-P<br>GPU<br>B |
| 20 | L20 | 80         | 18 not 19                         | USP<br>AT;<br>US-P<br>GPU<br>B |
| 21 | L21 | <b>286</b> | 17 and terminat\$4                | USP<br>AT;<br>US-P<br>GPU<br>B |

| ·  | L # | Hits | S arch Text                                         | DBs                            |
|----|-----|------|-----------------------------------------------------|--------------------------------|
| 22 | L22 | 104  | 21 and (impedance\$1 match\$3)                      | USP<br>AT;<br>US-P<br>GPU<br>B |
| 23 | L23 | 81   | 22 not (8 13 14 15 18)                              | USP<br>AT;<br>US-P<br>GPU<br>B |
| 24 | L24 | 182  | 21 not 22                                           | USP<br>AT;<br>US-P<br>GPU<br>B |
| 25 | L25 | 334  | 7 and (edge long)                                   | USP<br>AT;<br>US-P<br>GPU<br>B |
| 26 | L26 | 0    | 25 not (8 13 14 15 18<br>23 24)                     | USP<br>AT;<br>US-P<br>GPU<br>B |
| 27 | L27 | 4    | ("6545875" "6388886"<br>"3644661"<br>"6539449").pn. | USP<br>AT;<br>US-P<br>GPU<br>B |
| 28 | L28 | 2    | 27 and finger\$1                                    | USP<br>AT;<br>US-P<br>GPU<br>B |

|    | L#  | Hit | S arch Text                       | DBs                            |
|----|-----|-----|-----------------------------------|--------------------------------|
| 29 | L29 | 2   | 28 and terminat\$4                | USP<br>AT;<br>US-P<br>GPU<br>B |
| 30 | L30 | 2   | 29 and (impedance\$1<br>match\$3) | USP<br>AT;<br>US-P<br>GPU<br>B |
| 31 | L31 | 4   | 27 and edge                       | USP<br>AT;<br>US-P<br>GPU<br>B |
| 32 | L32 | 2   | 27 not 28                         | USP<br>AT;<br>US-P<br>GPU<br>B |
| 33 | L33 | 2   | 32 and edge                       | USP<br>AT;<br>US-P<br>GPU<br>B |
| 34 | L34 | 2   | 33 and terminat\$4                | USP<br>AT;<br>US-P<br>GPU<br>B |
| 35 | L35 | 1   | 34 and (impedance\$1 match\$3)    | USP<br>AT;<br>US-P<br>GPU<br>B |

|    | L#  | Hit | Search Text | DBs                            |
|----|-----|-----|-------------|--------------------------------|
| 36 | L36 | 1   | 34 not 35   | USP<br>AT;<br>US-P<br>GPU<br>B |